

PATENT

Attorney Docket No.: 728-208 (YOR9-2001-0270 US)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Louis L. HSU, et al. **GROUP ART UNIT:** 2814
APPLN NO: 09/880,223 **EXAMINER:** Rao, Shrinivas
FILING DATE: June 13, 2001 **DATED:** July 16, 2004
FOR: **T-RAM CELL HAVING A BURIED VERTICAL
THYRISTOR AND A PSEUDO-TFT TRANSFER GATE AND
A METHOD FOR FABRICATING THE SAME**


Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

ASSOCIATE POWER OF ATTORNEY (37 C.F.R. §1.34)

Sir:

Please recognize **Jeff Kirshner**, Reg. No. 53,137, of
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as associate attorney in the above referenced application, to make alterations and amendments
therein and transact all business connected with it in the Patent and Trademark Office.

Respectfully submitted,


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